



NOTES:

1. Mfr. RCA Solid State Division
2. Die size .052 x .048 ± .002

FIGURE 2
DIE CONFIGURATION

3.2 Electrical Requirements

Each die shall be tested by probing and meet the performance requirement at 25 degrees C as specified in Table I and be capable of meeting the requirements specified for test temperatures of -55 degrees C and +125 degrees C.

TABLE I
ELECTRICAL CHARACTERISTICS

recommended operating conditions

		SN54HC'			SN74HC'			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	Supply voltage	2	5	6	2	5	6	V
V _{IH}	High-level input voltage	V _{CC} = 2 V		1.5	1.5			V
		V _{CC} = 4.5 V		3.15	3.15			
		V _{CC} = 6 V		4.2	4.2			
V _{IL}	Low-level input voltage	V _{CC} = 2 V		0	0.3	0	0.3	V
		V _{CC} = 4.5 V		0	0.9	0	0.9	
		V _{CC} = 6 V		0	1.2	0	1.2	
V _I	Input voltage	0		V _{CC}		V _{CC}		V
V _O	Output voltage	0		V _{CC}		V _{CC}		V
t _t	Input transition (rise and fall) times (except Schmitt-trigger inputs)	V _{CC} = 2 V		0	1000	0	1000	ns
		V _{CC} = 4.5 V		0	500	0	500	
		V _{CC} = 6 V		0	400	0	400	
T _A	Operating free-air temperature	-55		125	-40		85	°C

39126-003 A

ICII Integrated Circuits Incorporated
10301 WILLOWS ROAD, REDMOND, WA 98052

SIZE
A

FSCM NO.
50821

DRAWING NO.
85230

REV
A

SCALE

SHEET 3 OF 4